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# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: JaiPrakash et al. Art Unit: Not yet assigned  
Serial No.: 09/756,415 Examiner: Not yet assigned  
Filed: January 8, 2001 Atty Dkt No: 01P7407US  
For: Self-Aligned Collar and Strap Date: March 19, 2001  
Formation For Semiconductor Devices

## LETTER TO OFFICIAL DRAFTSMAN

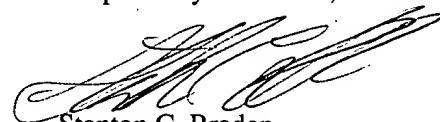
Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

Pursuant to M.P.E.P. Section 608.02, enclosed please find twenty four (24) sheets of formal drawings in the referenced case. These are formal versions of the informal drawings filed with the application. No changes have been made to the drawings.

If there are any informalities with these drawings, Applicant requests issuance of a Form PTO-948 identifying the informalities noted by the Official Draftsman.

Respectfully submitted,



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Reg. No. 32,556

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c/o Siemens Corporation  
Intellectual Property Department  
186 Wood Avenue South  
Iselin, NJ 08830  
(732) 321-3150

## CERTIFICATE OF MAILING PURSUANT TO 37 CFR § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents  
Washington, D.C. 20231 on or before the date below

Date of  
Signature March 19, 2001

Signed



Reg. # 32,556

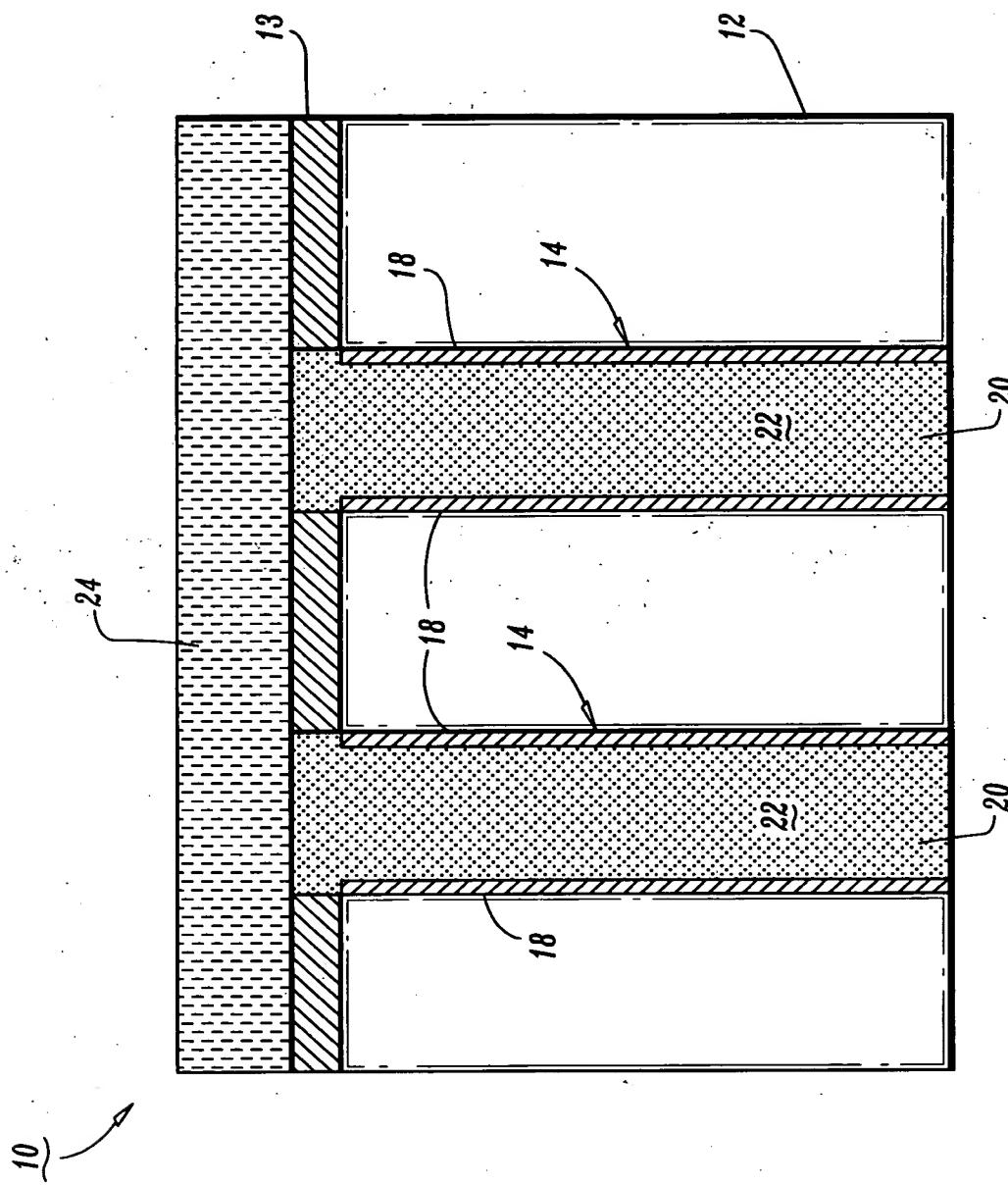


FIG. 1

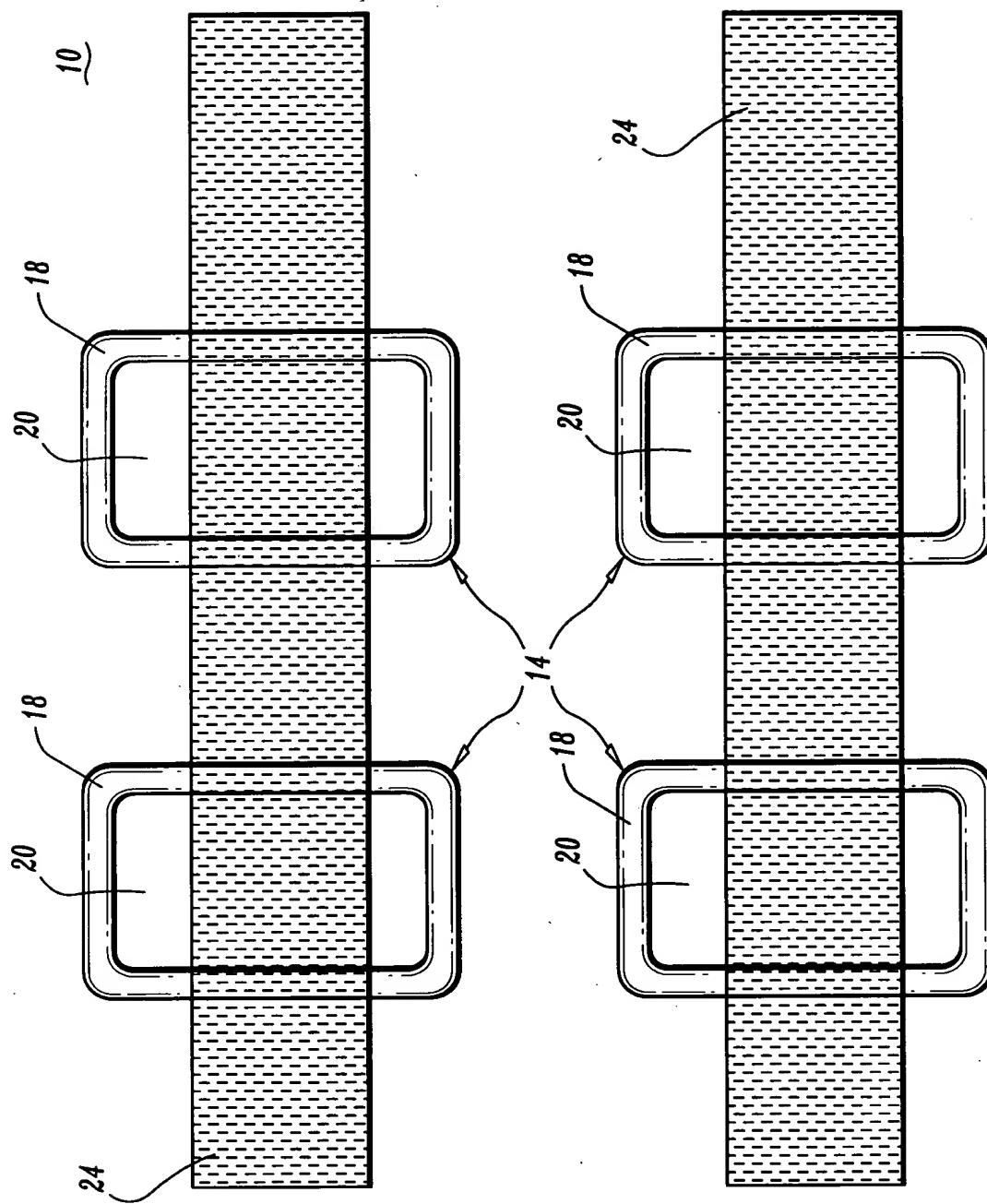


FIG. 2

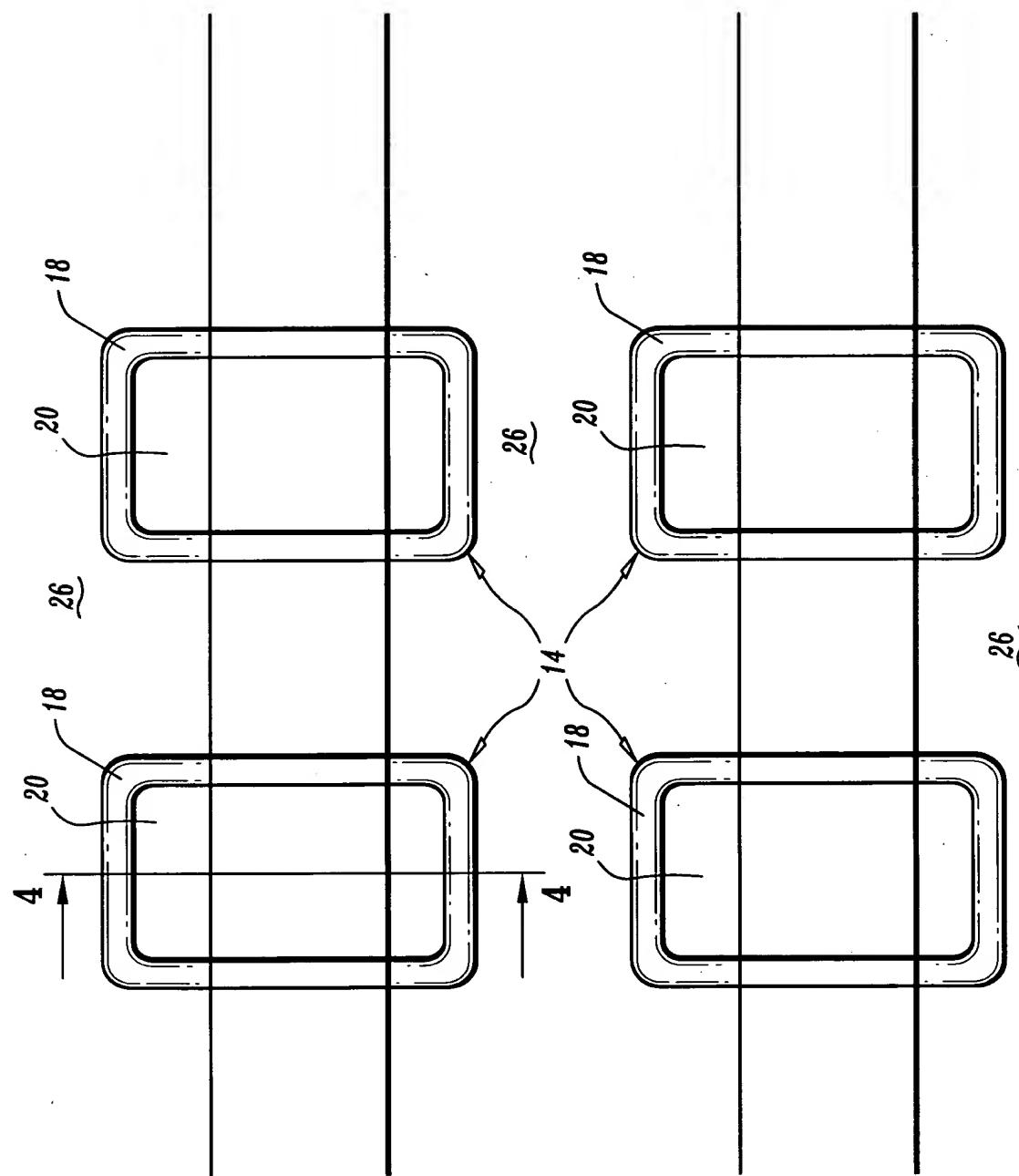


FIG. 3

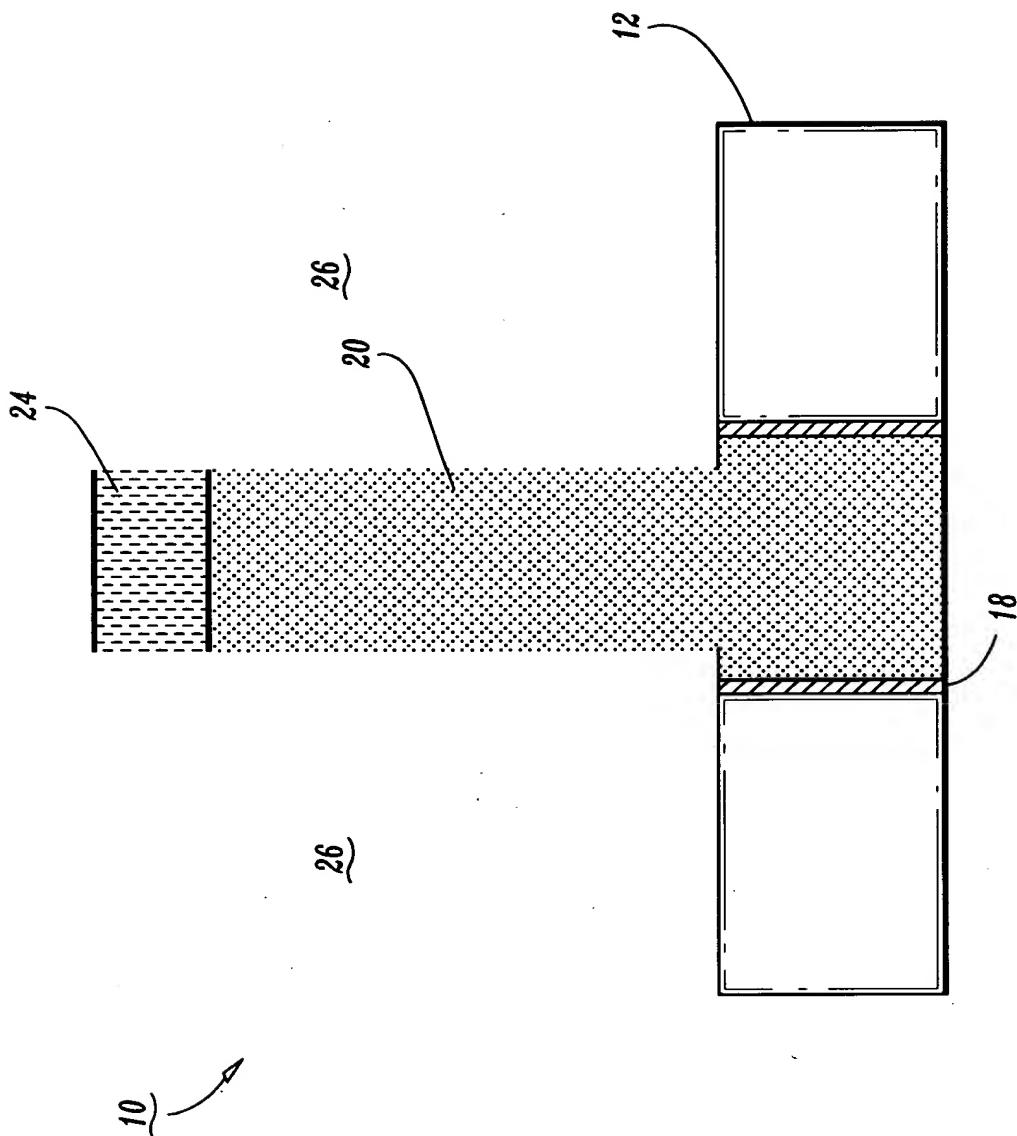


FIG. 4

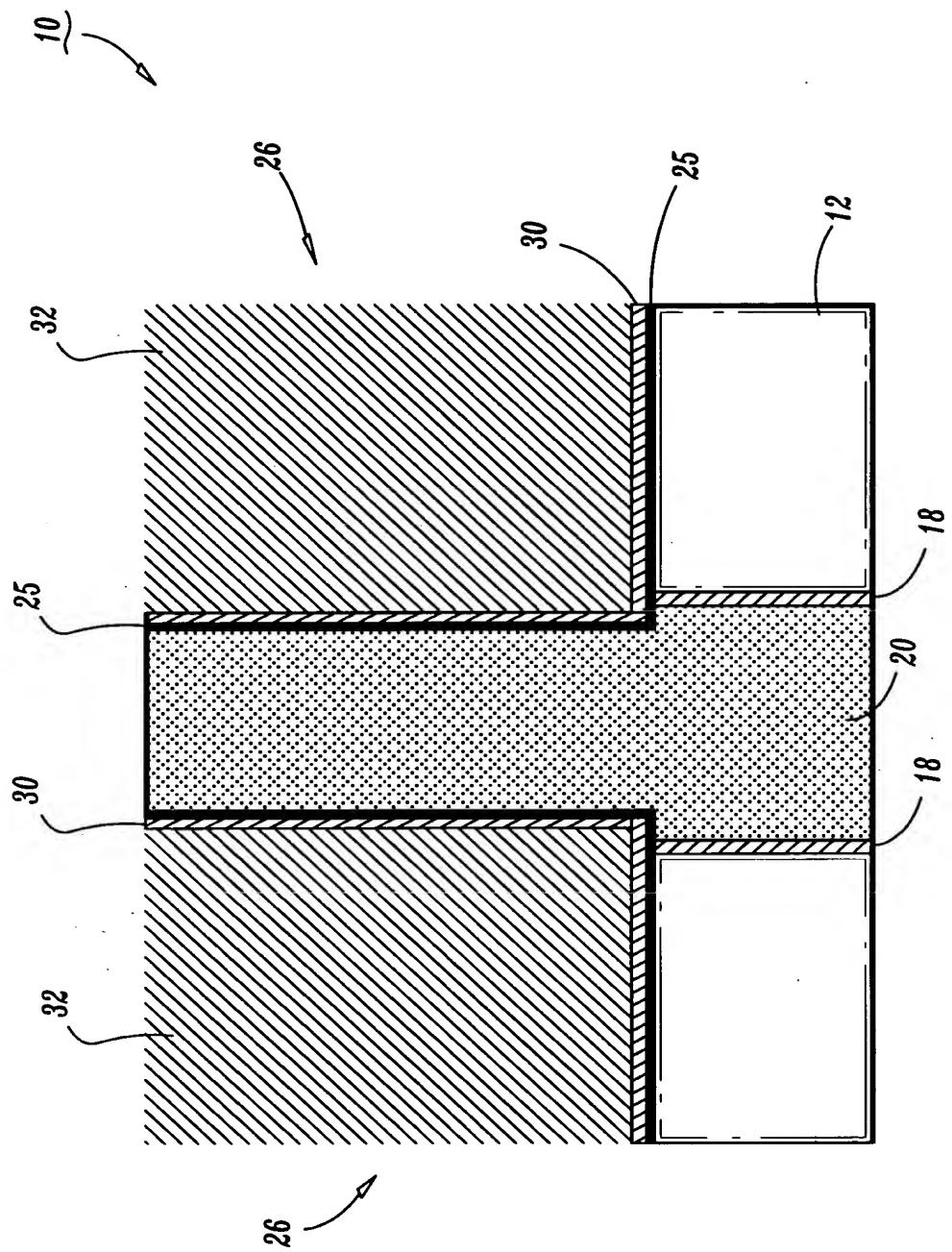


FIG. 5

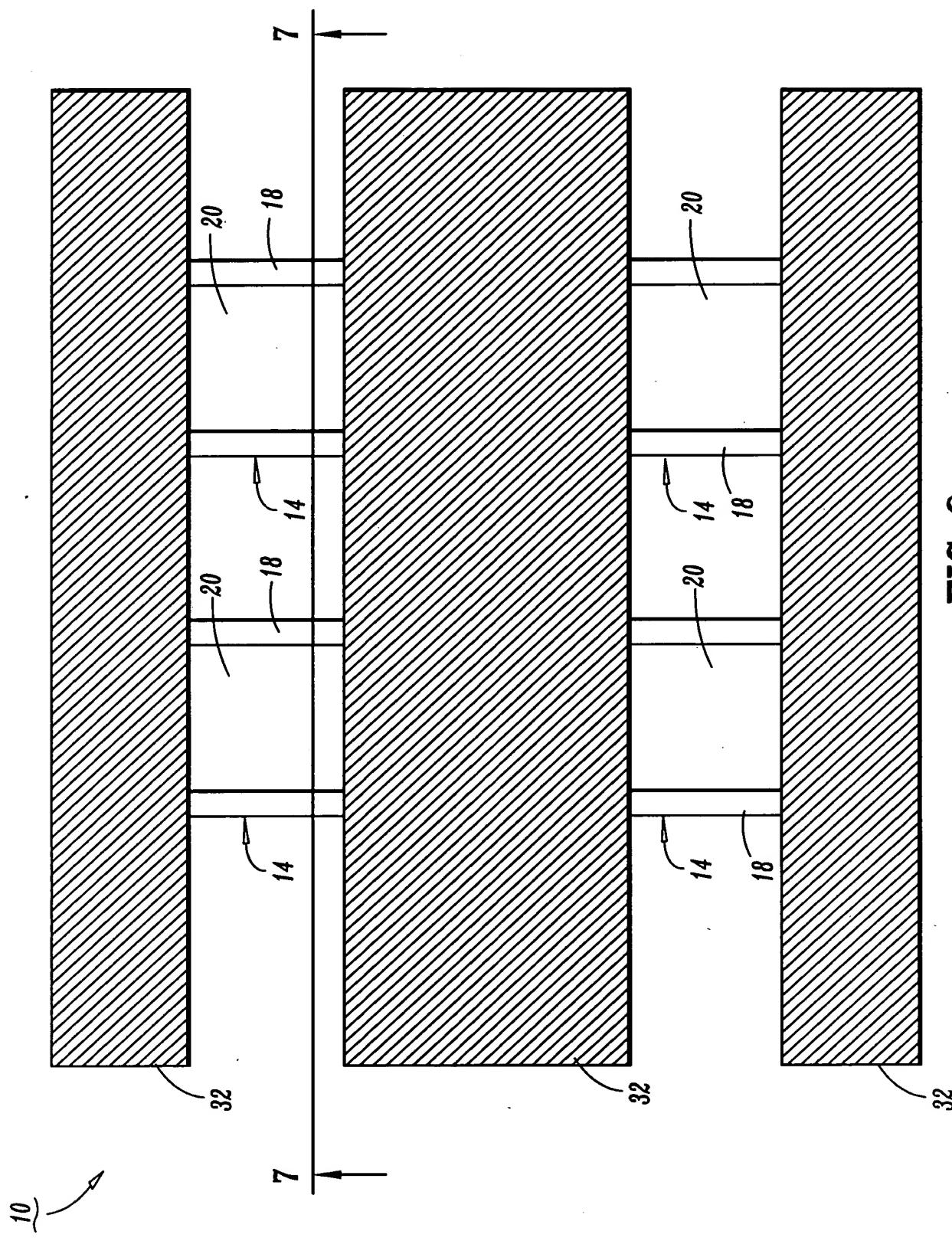


FIG. 6

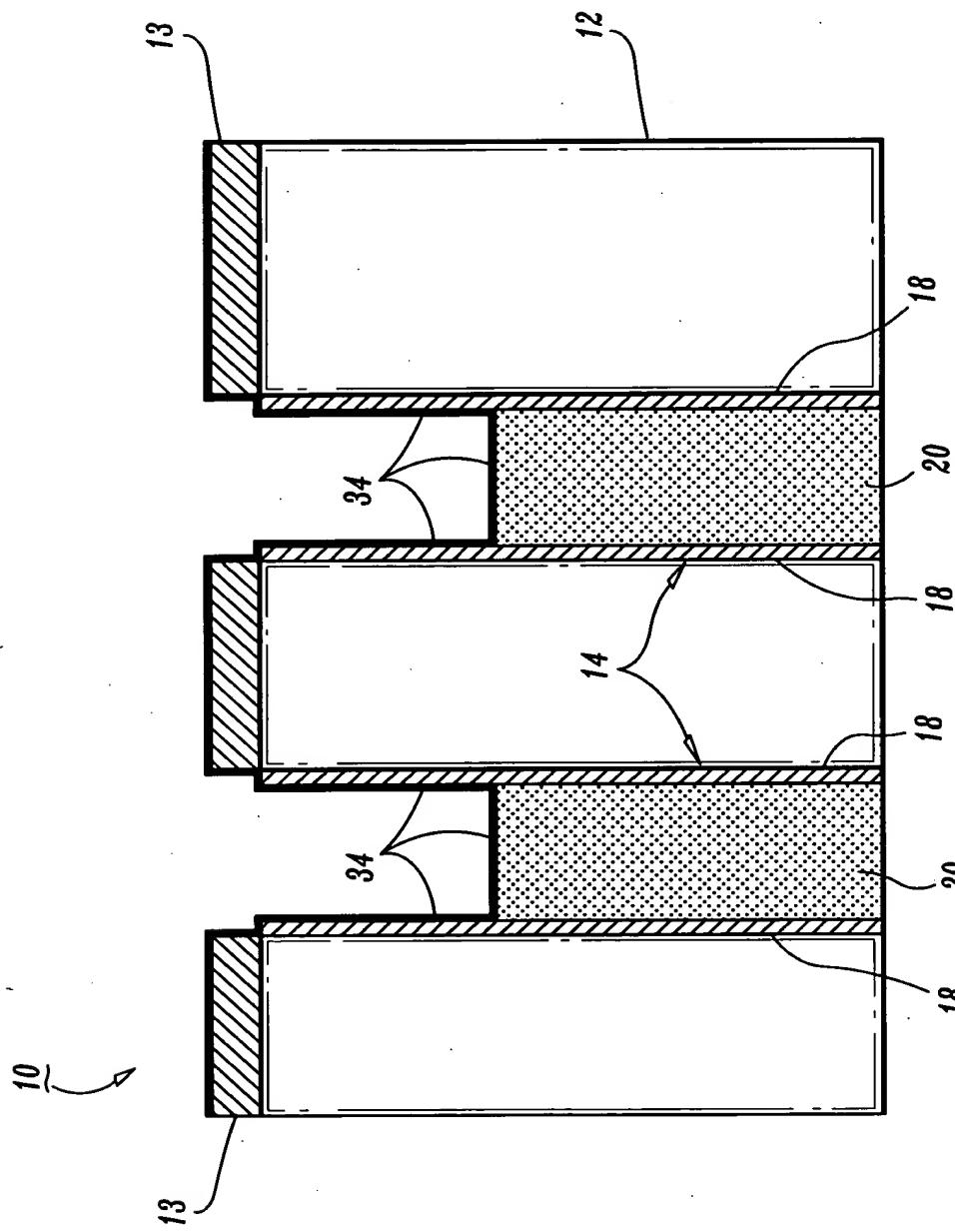


FIG. 7

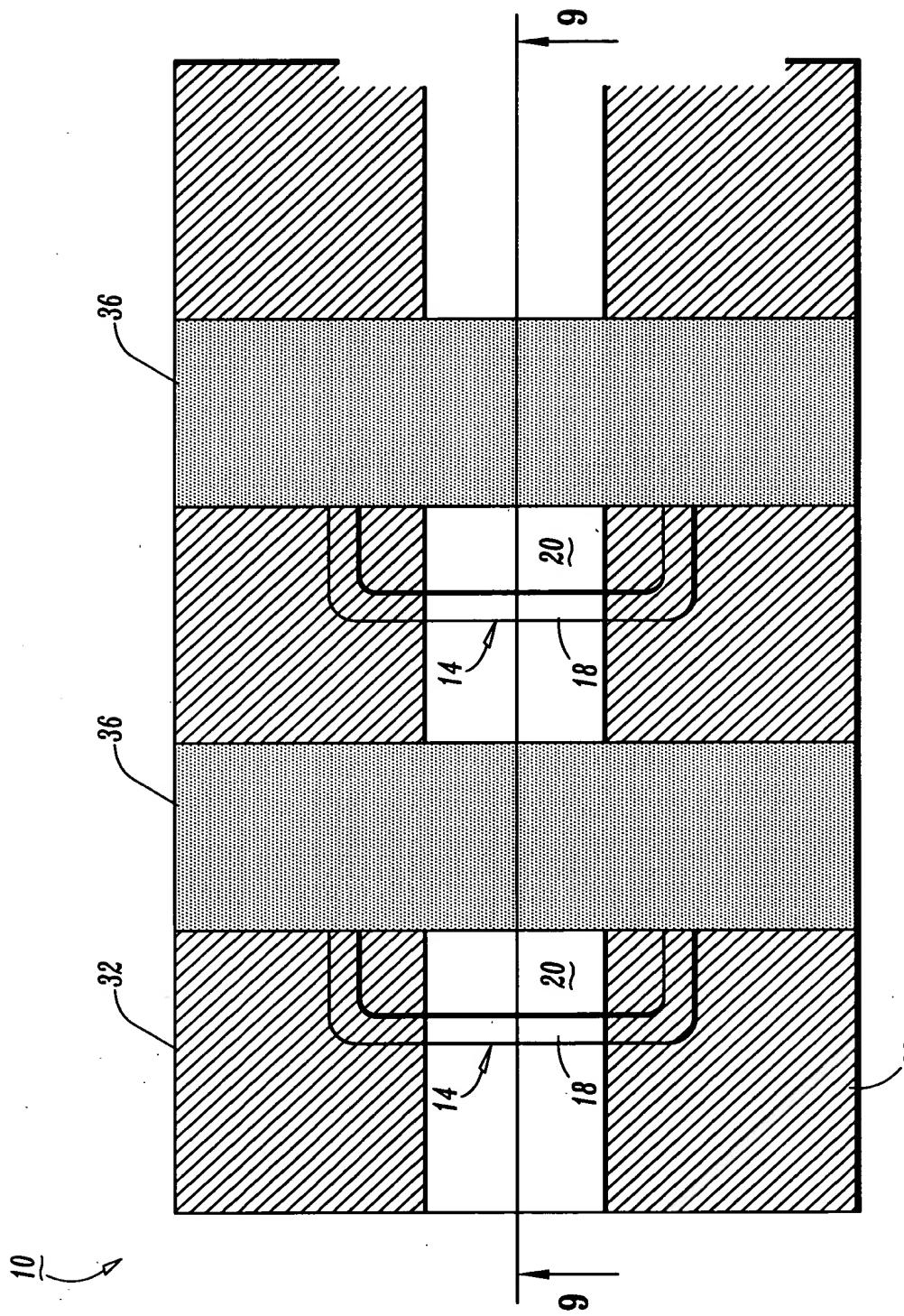


FIG. 8

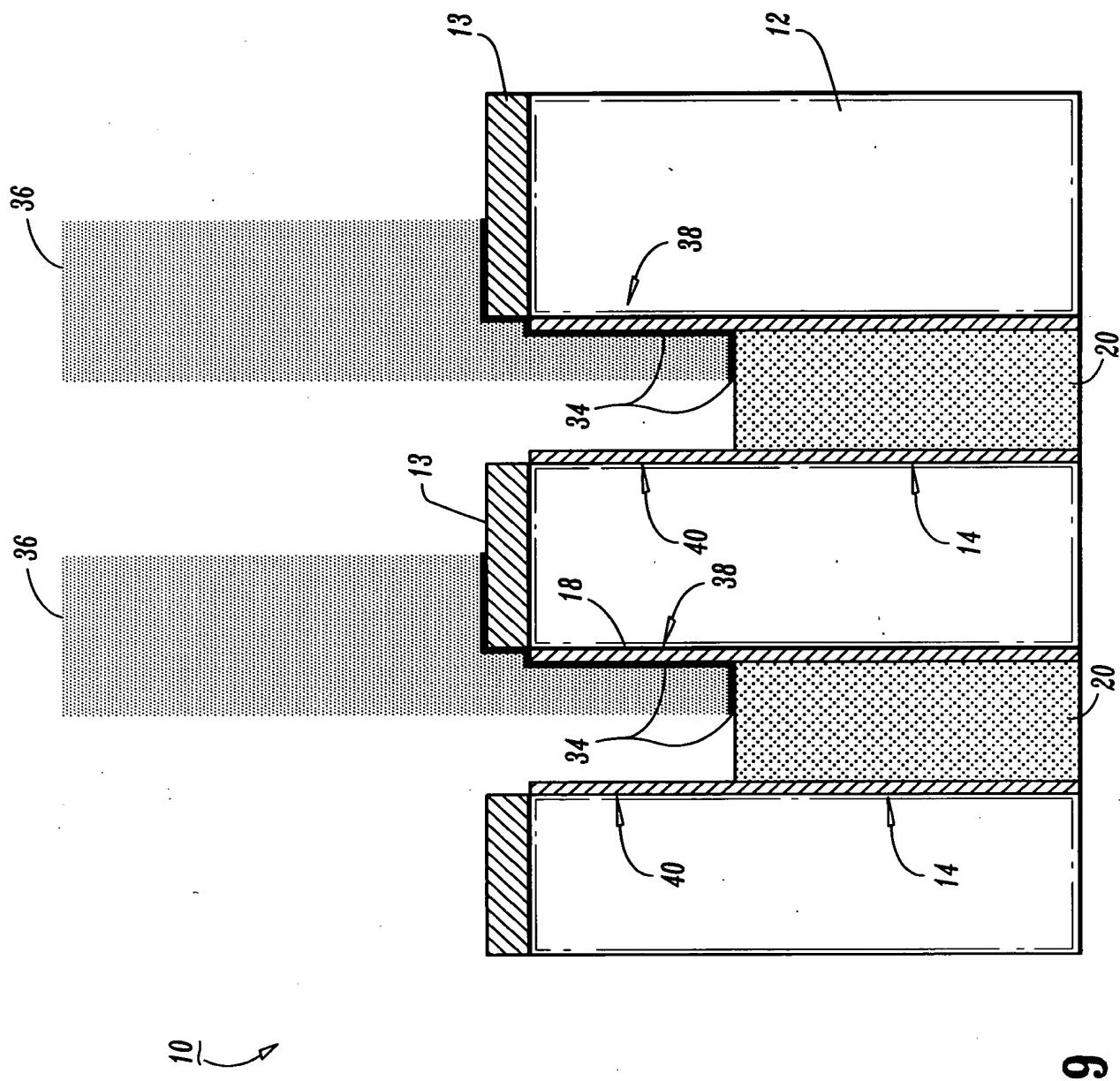


FIG. 9

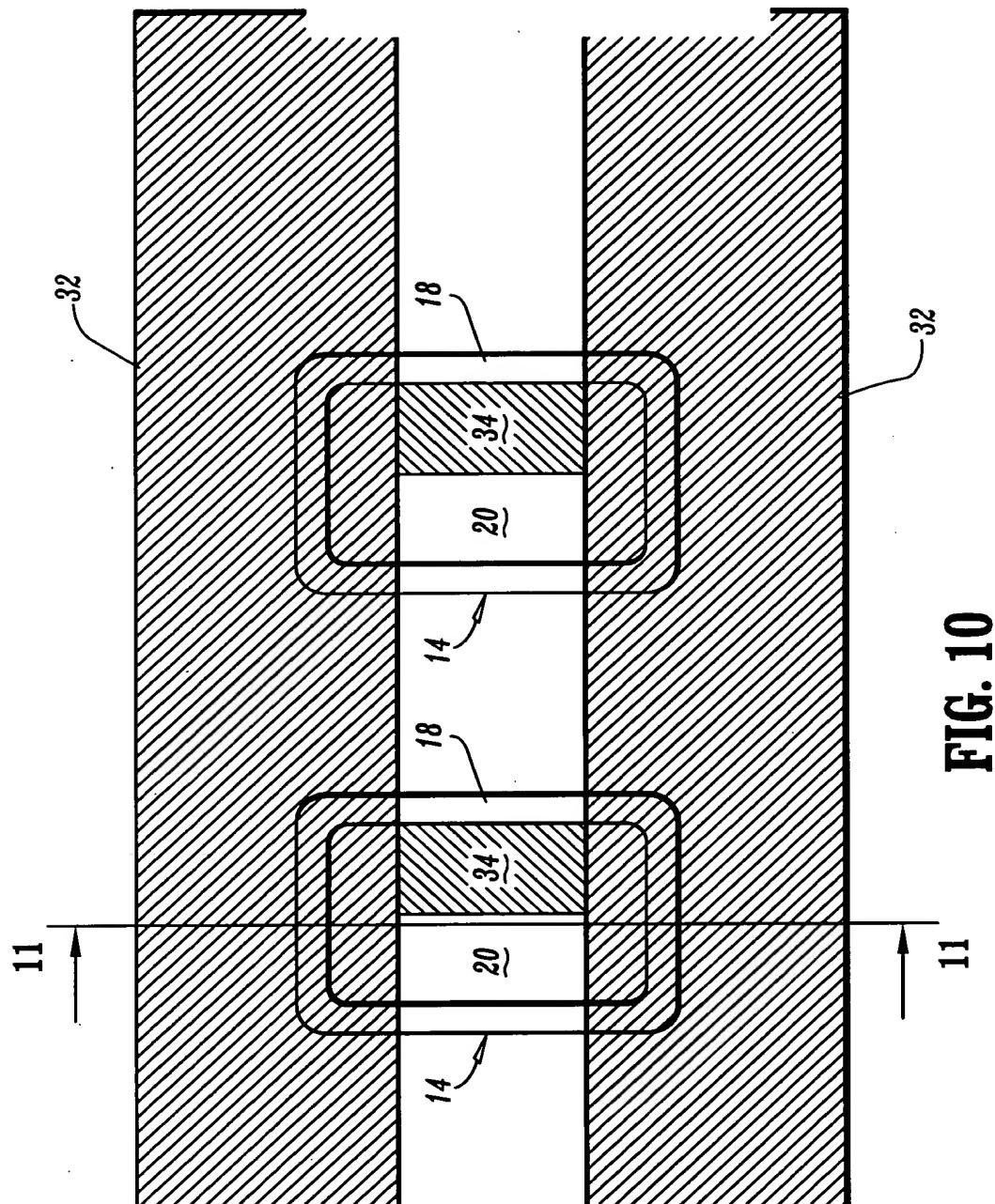


FIG. 10

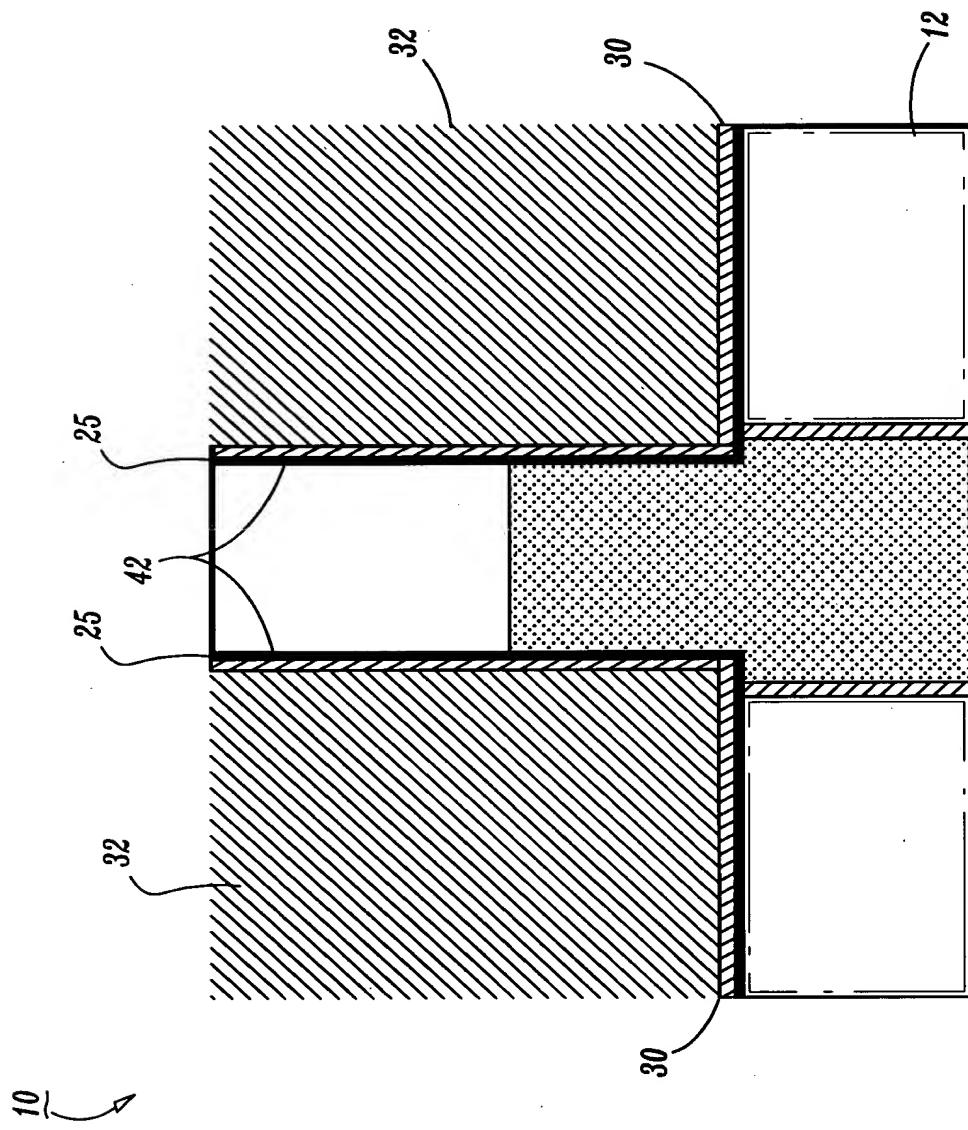


FIG. 11

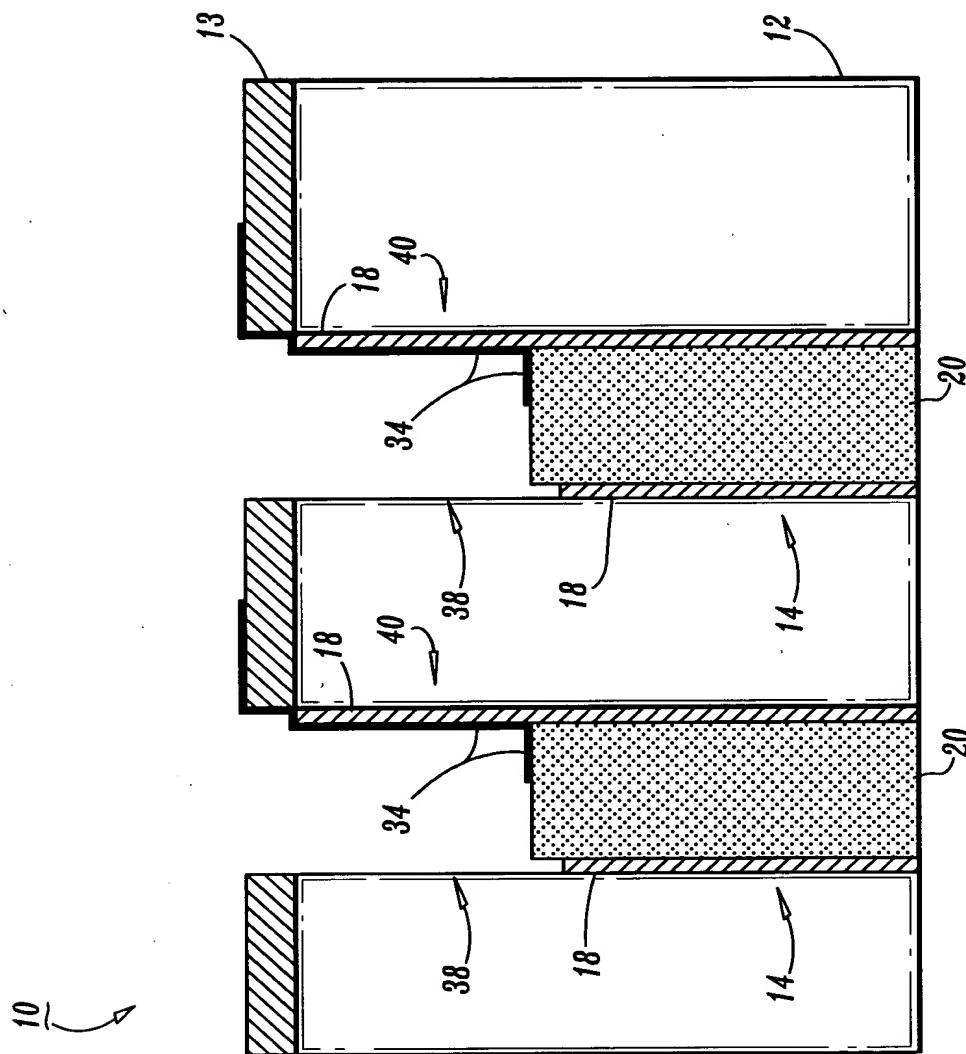


FIG. 12

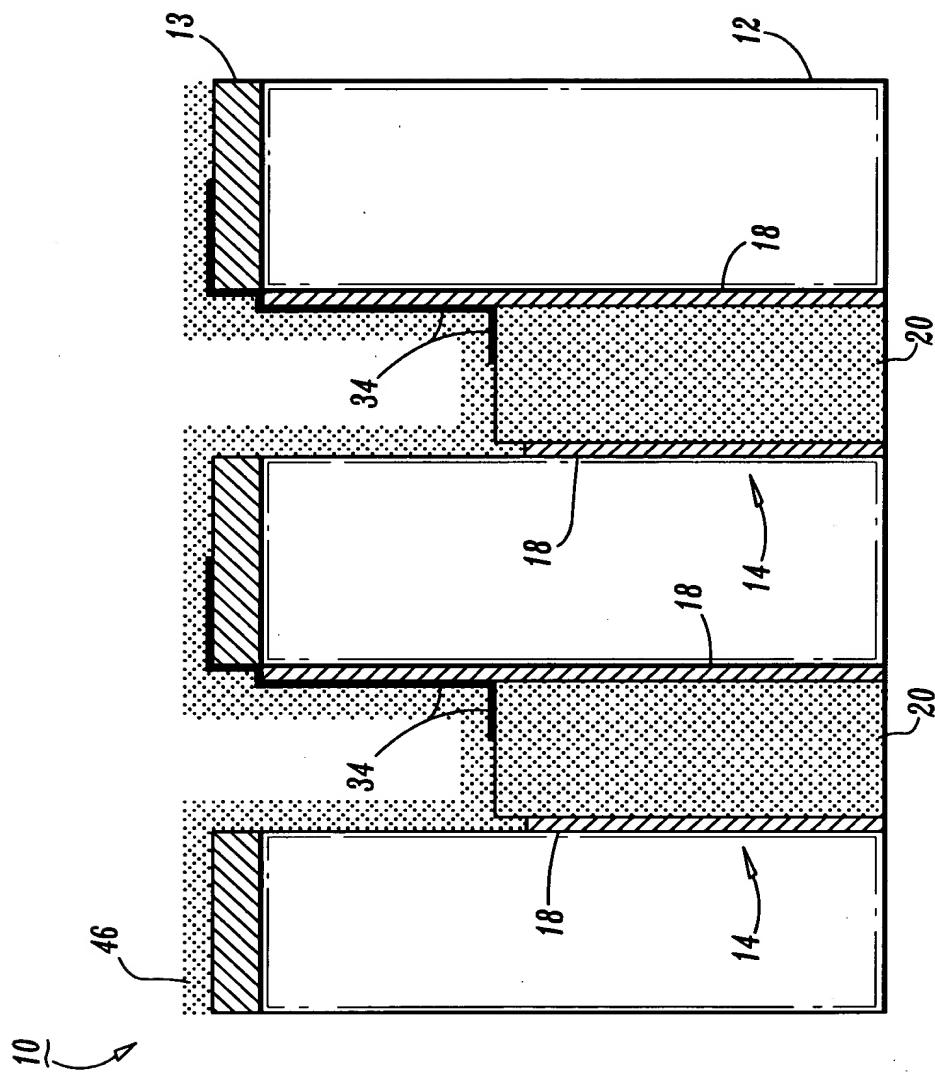


FIG. 13

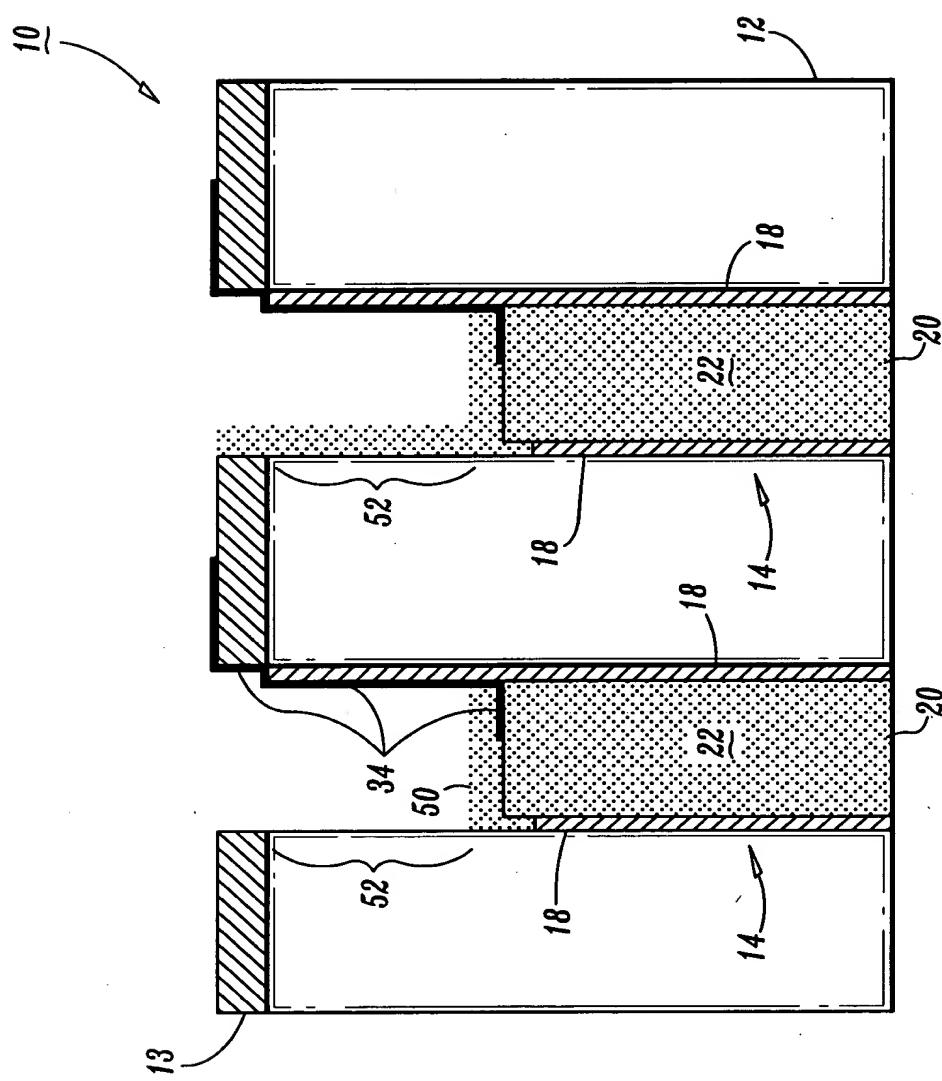


FIG. 14

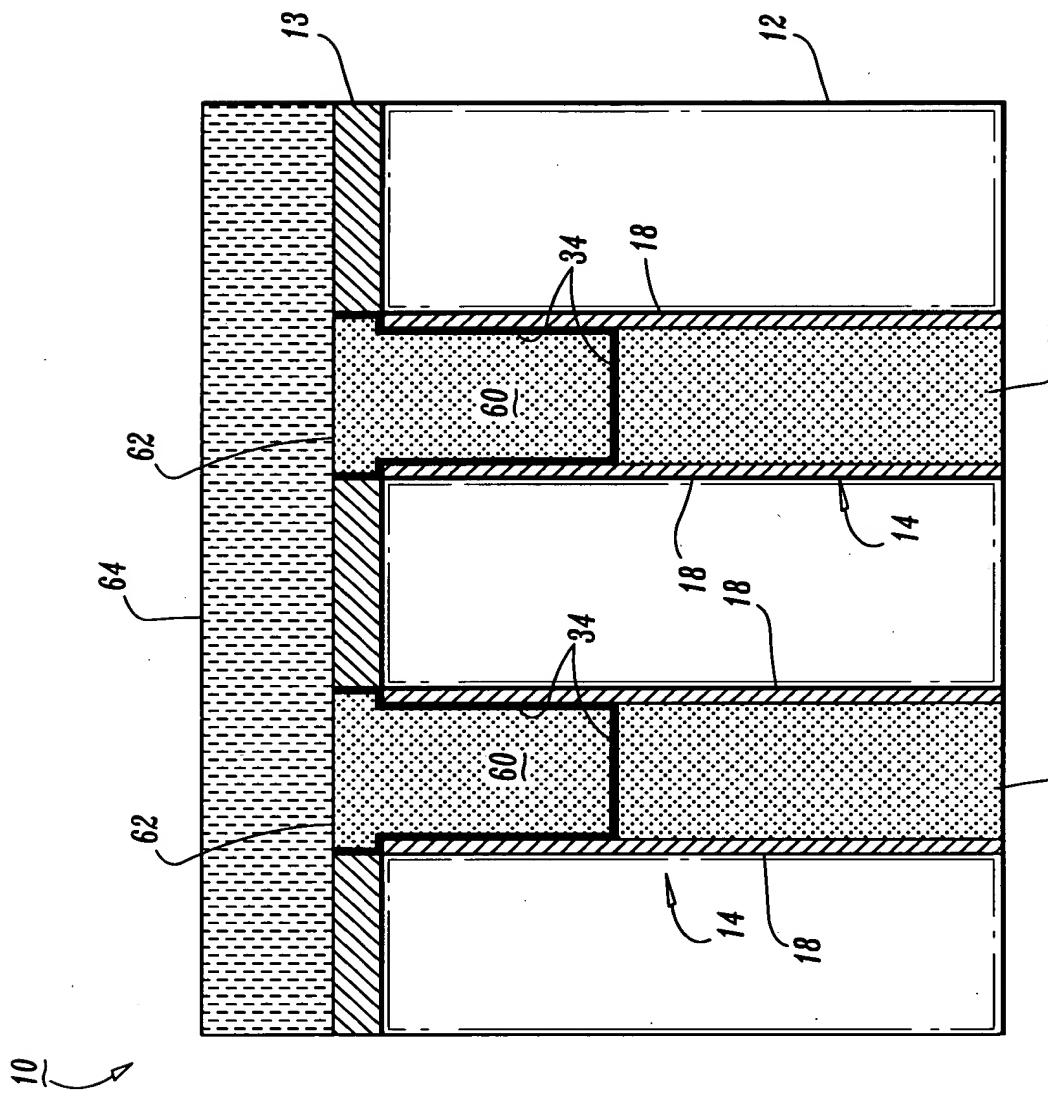


FIG. 15

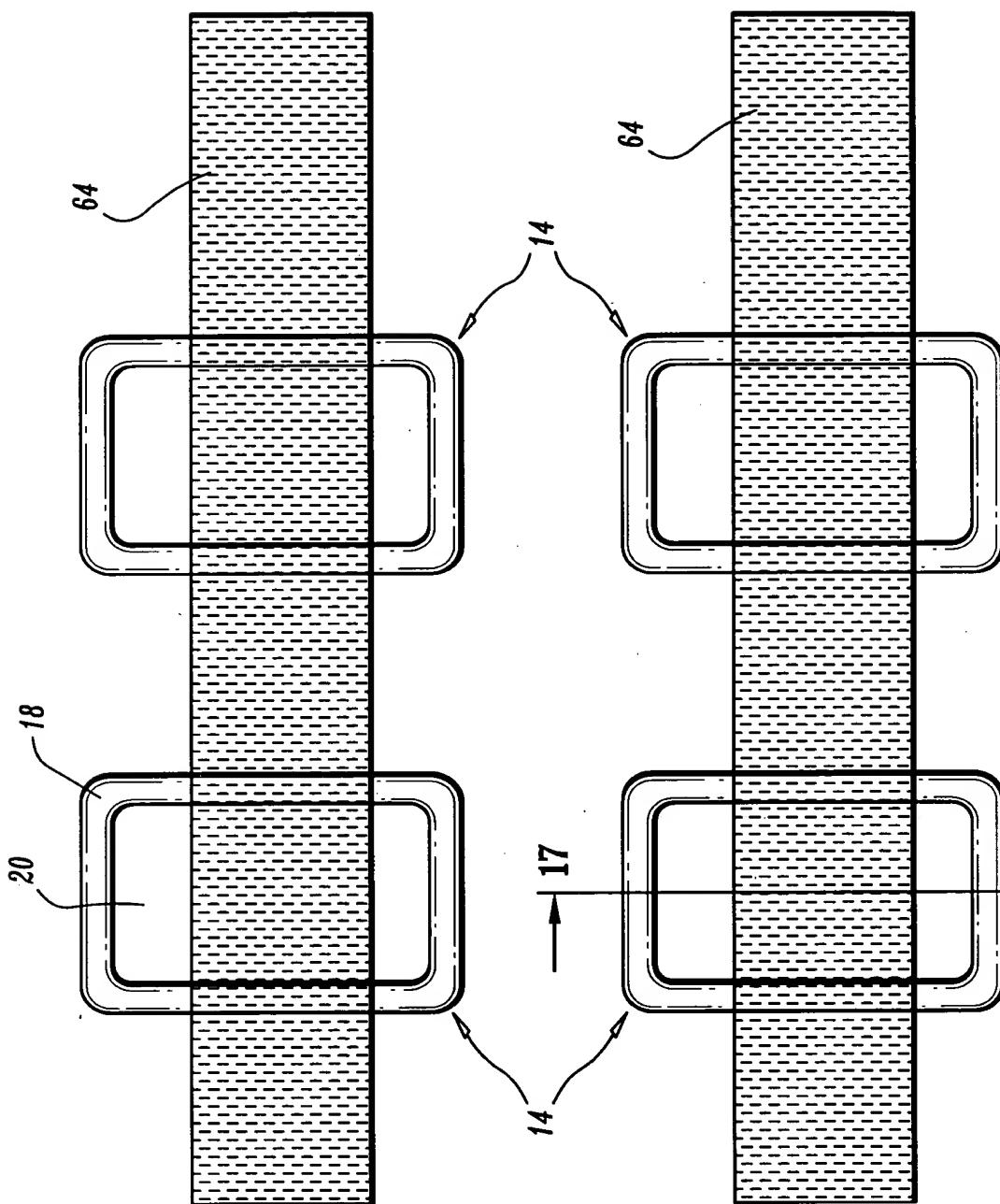


FIG. 16

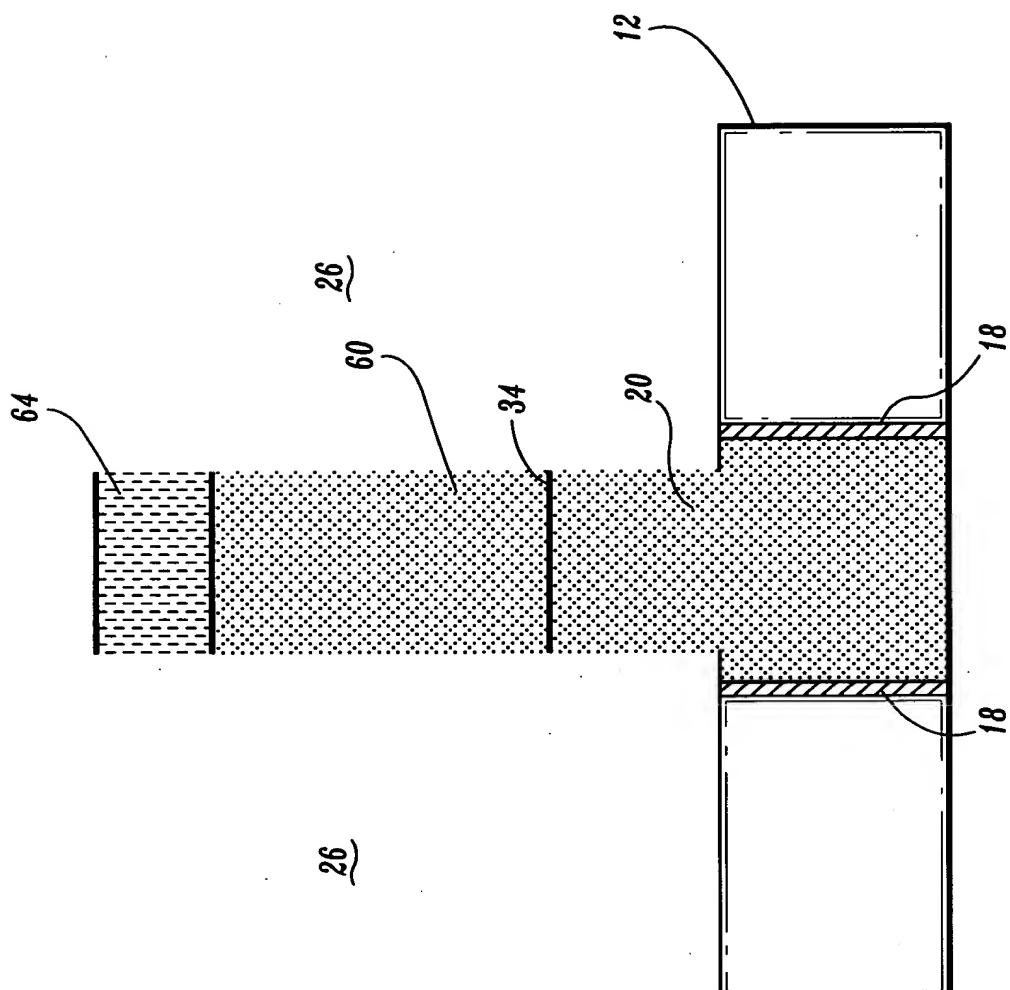


FIG. 17

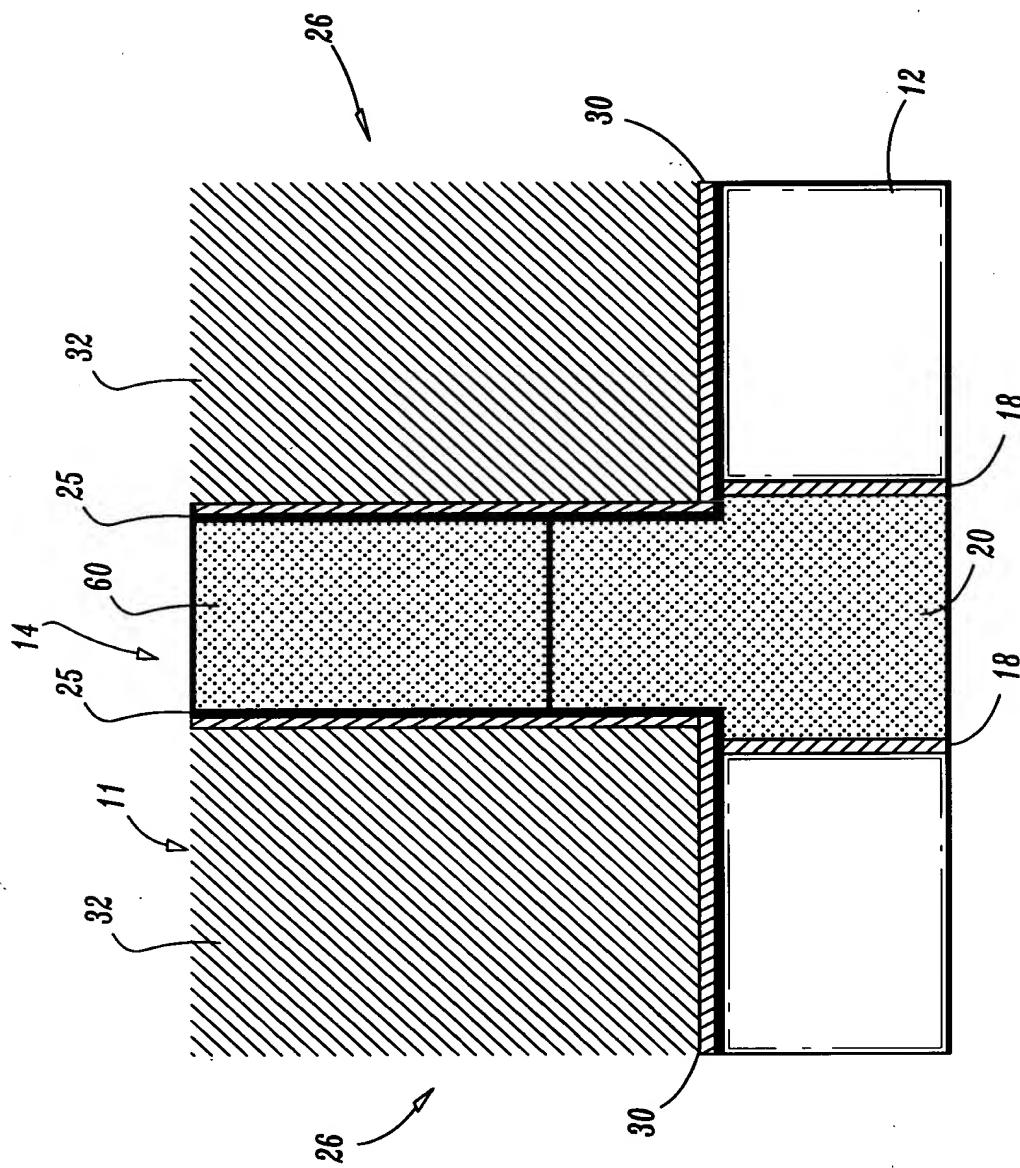


FIG. 18

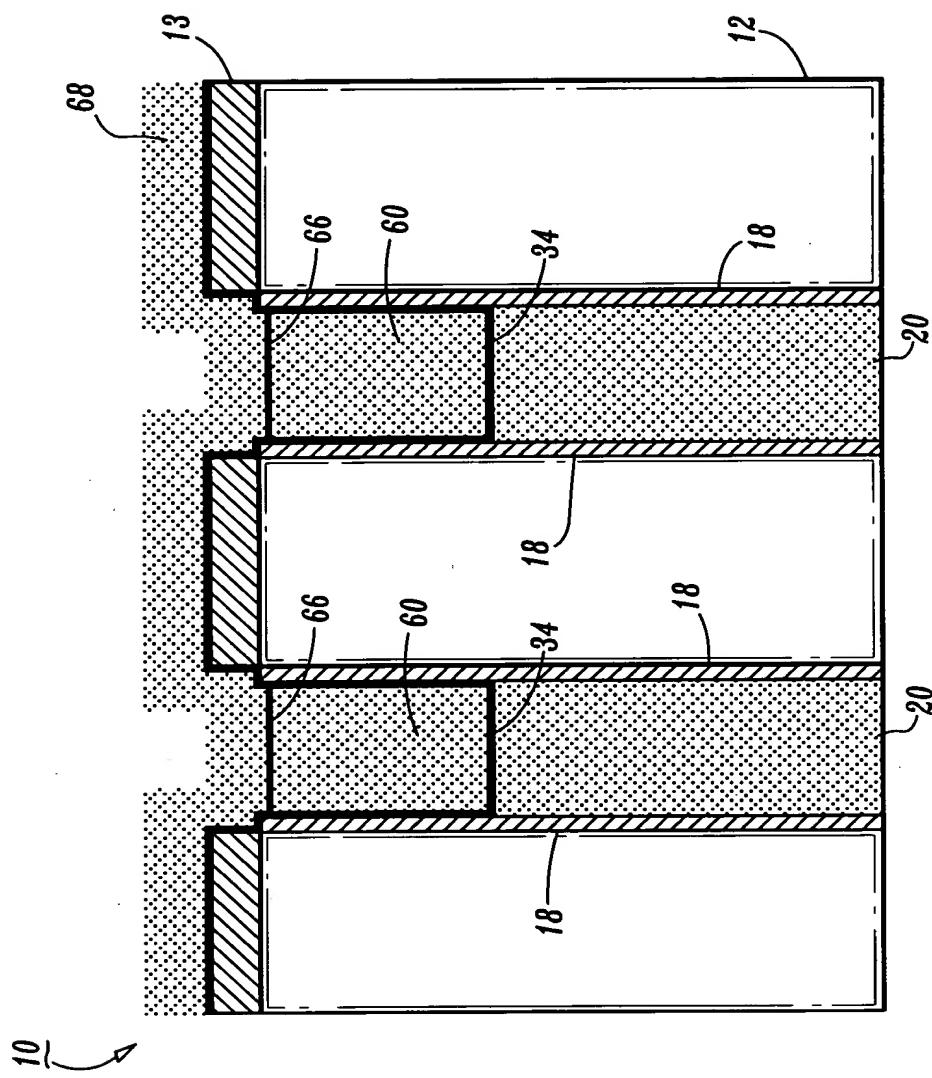
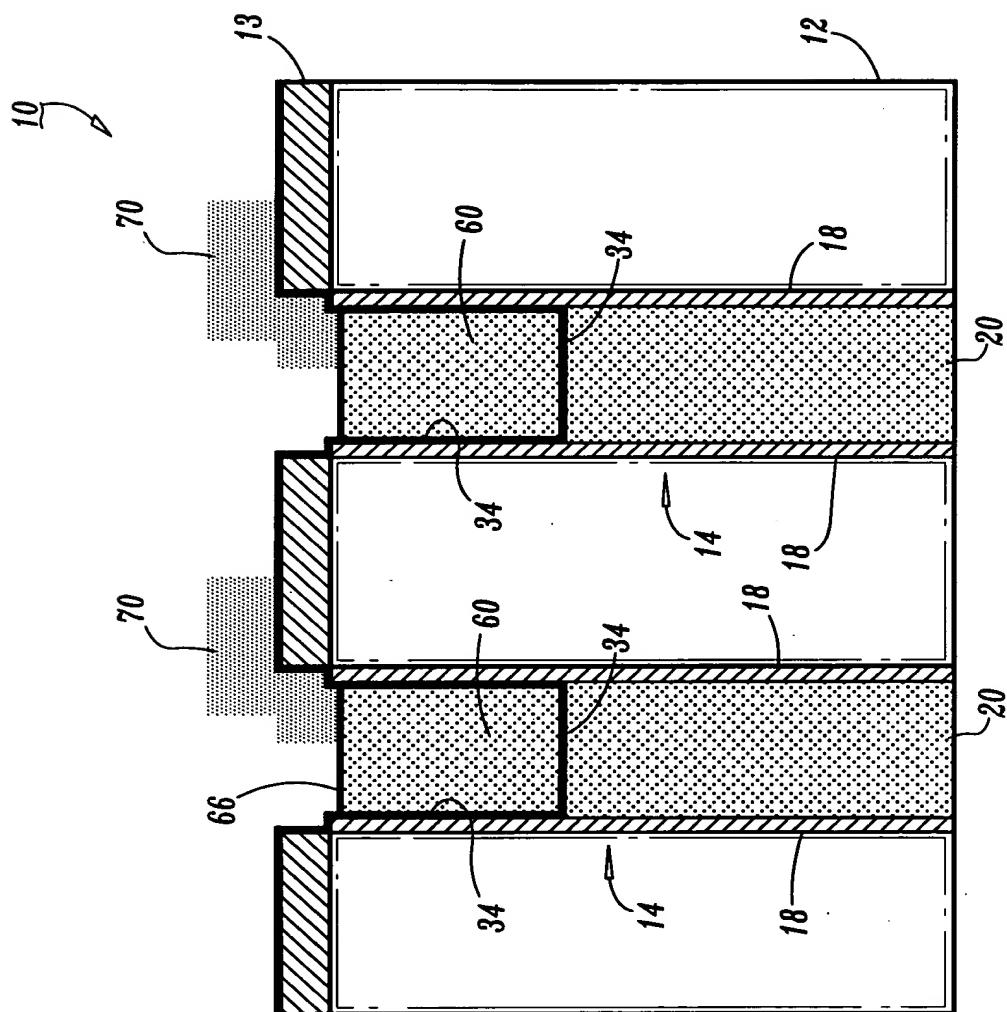
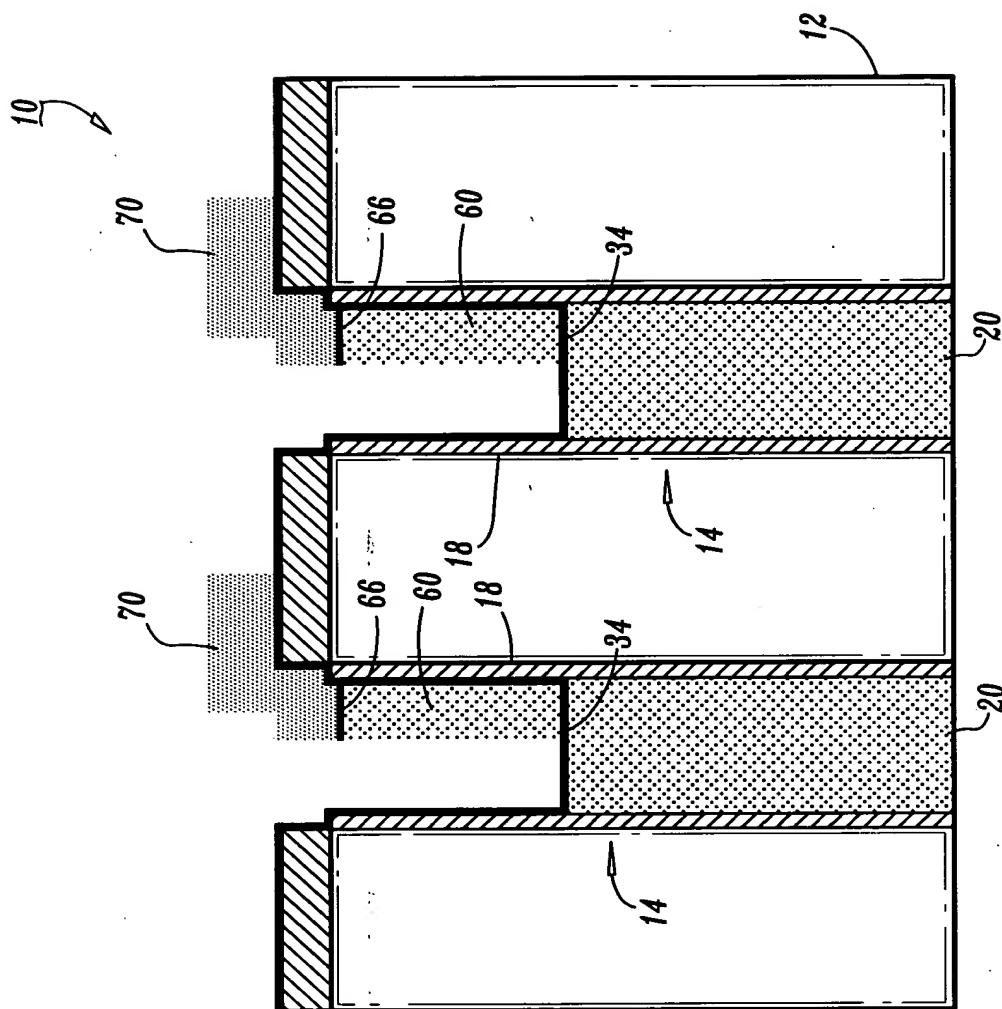


FIG. 19



**FIG. 20**



**FIG. 21**

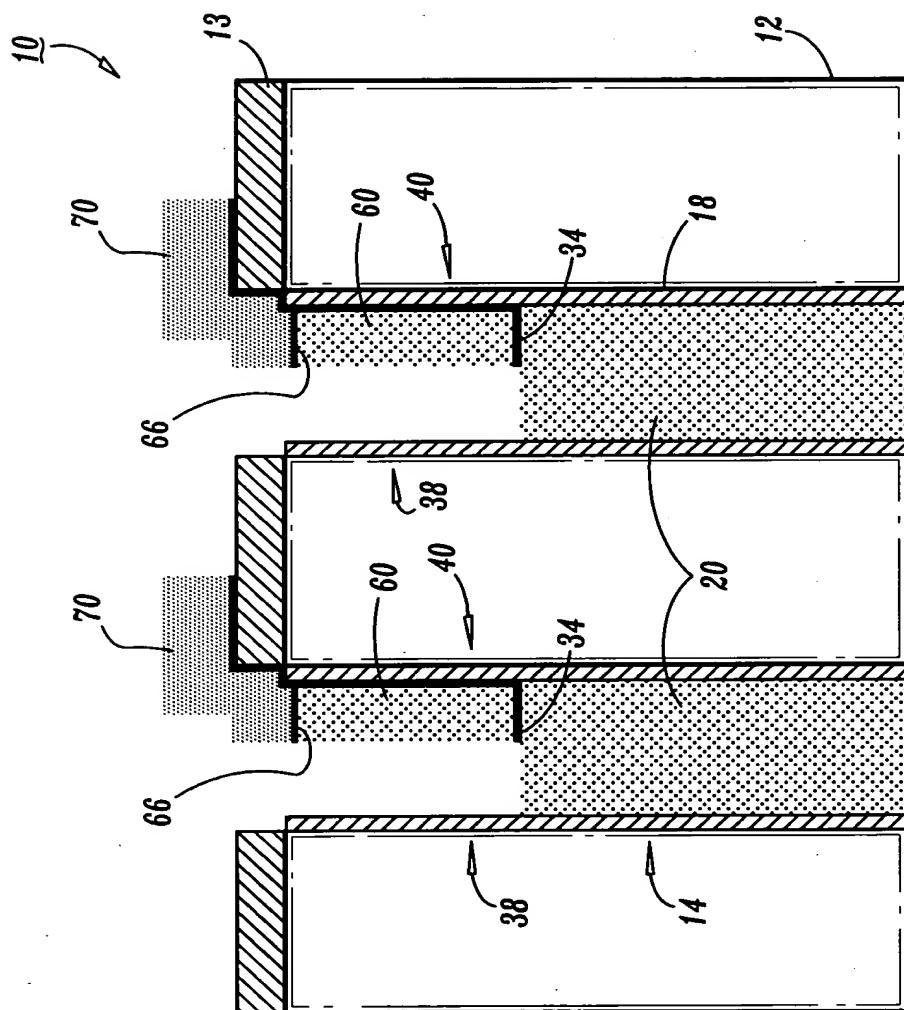


FIG. 22

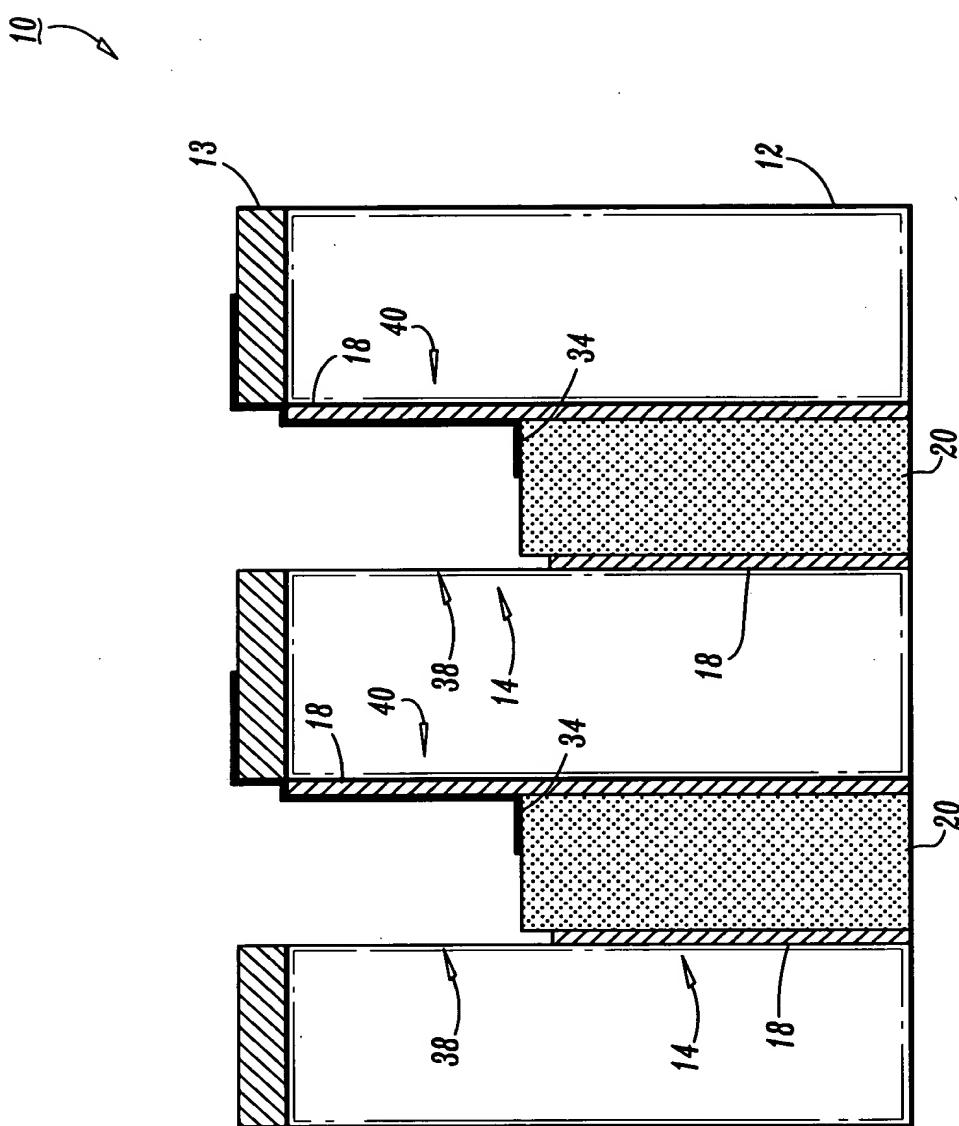


FIG. 23

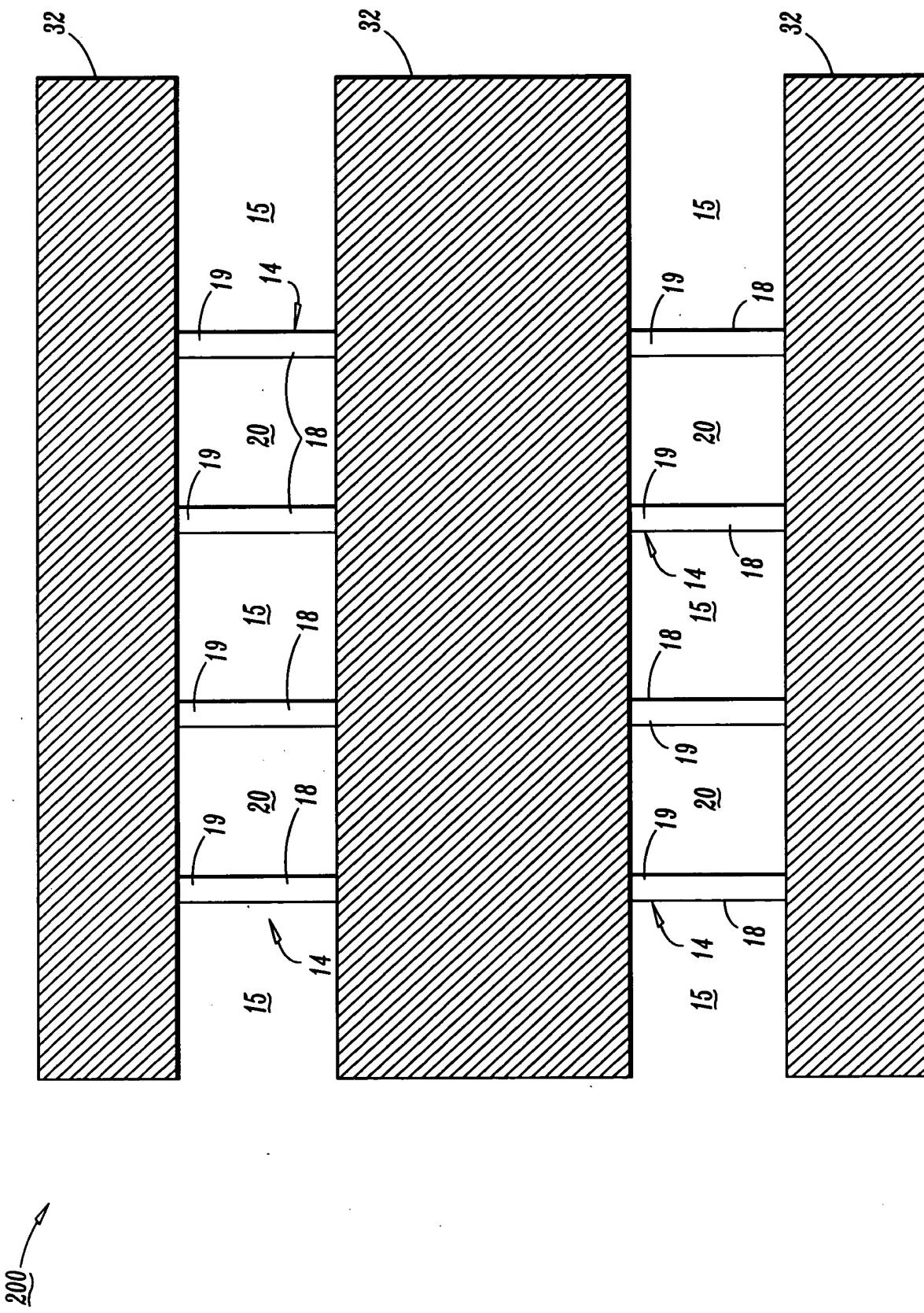


FIG. 24